

High Input Voltage SMPS Start-up / Linear Regulator

Ordering Information

Order Number / Package					
TO-92	TO-243AA*	TO-220	8-Pin P-DIP	SO-8	Die
LR645N3	LR645N8	LR645N5	LR645N4	LR645LG	LR645ND

*Same as SOT-89.

Features

- ☐ Accepts inputs from 15V to 450V
- ☐ Output currents to 3mA continuous, 30mA peak
- ☐ Supply current typically 50 μ A
- ☐ Line regulation typically 0.1mV/V
- ☐ Load regulation typically 50mV/mA
- ☐ Ripple rejection typically 60dB
- ☐ Output can be trimmed from 8.0V to 12V
- ☐ Output current can be increased to 150mA with external FET

Applications

- ☐ Off-line SMPS startup circuits (pulse loads)
- ☐ Low power off-line regulators
- ☐ Regulators for noisy inputs

Caution

The LR6 does NOT provide galvanic isolation. When operated from an AC line, potentially lethal voltages can be present on the IC. Adequate means of protecting the end user from such voltages must be provided by the circuit developer.

Absolute Maximum Ratings

Input Voltage	450V
Output Voltage	15.5V
Operating and Storage Temperature	-55°C to +150°C
Soldering Temperature*	300°C

*Distance of 1.6mm from case for 10 seconds

General Description

The Supertex LR6 is a high input voltage, low output current linear regulator. It has a 3-terminal fixed output voltage version available in TO-92, TO-220 and SOT-89 packages as well as an adjustable voltage version available in 8 pin DIP and SOIC packages. The 3-terminal version functions like any other low voltage 3-terminal regulator except it allows the use of much higher input voltages. When used in a SMPS start-up circuit, it eliminates the need for large power resistors. In this application, current is drawn from the high voltage line only during start-up. Only leakage current flows after start-up thereby reducing the continuous power dissipation to a few milliwatts.

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Pin Configuration

TO-243AA
(SOT-89)

TO-92

TO-220

P-DIP

SO-8

	+V _{IN}	GND	V _{OUT}	Trim	Gate
TO-92	1	2	3	—	—
TO-243AA	1	2	3	—	—
TO-220	1	2	3	—	—
P-DIP	1	3	4	5	7
SO-8	1	3	4	5	7

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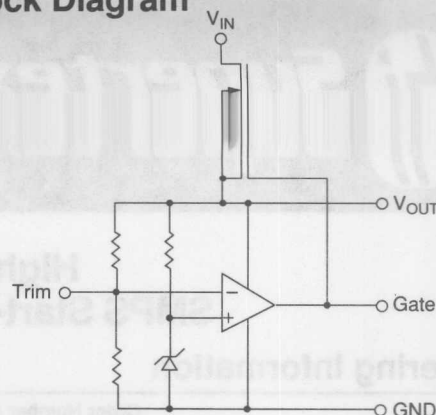
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Thermal Characteristics

Package	Power Dissipation @ $T_A = 25^\circ\text{C}$	θ_{jc} $^\circ\text{C/W}$	θ_{ja} $^\circ\text{C/W}$
SO-8	0.31W	156	400 [†]
P-DIP	0.78W	125	160
TO-92	0.74W	125	170
TO-220	1.8W	8.3	70
TO-243AA	1.6	15	78 [†]

[†] Mounted on FR5 board, 25mm x 25mm x 1.57mm.
Significant P_D increase possible on ceramic substrate.

Block Diagram



Electrical Characteristics

Test conditions unless otherwise specified: $T_A = 25^\circ\text{C}$; $V_{IN} = 15$ to 450V , $C_{OUT} = 0.01\mu\text{F}$

Symbol	Parameter	Min	Typ	Max	Unit	Conditions
V_{OUT}	Output Voltage	9.3	10	10.7	V	No load
V_{OUT}	Output Voltage over Temperature ¹	9.0	10	11.5	V	$T_J = -40^\circ\text{C} \leq \text{to} + 125^\circ\text{C}$, No load
ΔV_{OUT}	Line Regulation		40	100	mV	$V_{IN}=15\text{V}$ to 450V , No load
ΔV_{OUT}	Load Regulation ¹		150	400	mV	$V_{IN}=50\text{V}$, $I_{OUT}=0$ to 3.0mA
V_{IN}	Operating Input Voltage Range	15		450	V	
I_{INQ}	Input Quiescent Current		50	150	μA	No Load
I_{OFF}	V_{IN} Off-State Leakage Current		0.1	10	μA	$V_{AUX} \geq V_{OUT} + 1\text{V}$ applied to V_{OUT} pin
I_{AUX}	Input Current to V_{OUT}		50	150	μA	$V_{AUX} \geq V_{OUT} + 1\text{V}$ applied to V_{OUT} pin
$\Delta V_{OUT}/\Delta V_{IN}$	Ripple Rejection Ratio ¹	50	60		dB	120Hz, No Load
en	Noise voltage ¹		25		μV	0.01 to 100KHz
I_{PEAK}	Output Peak Current ²		30		mA	$C_{OUT} = 10\mu\text{F}$, $V_{IN} = 400\text{V}$
V_{AUX}	External Voltage Applied to V_{OUT}			13.2	V	

8-pin, adjustable output voltage version only.

Test conditions unless otherwise specified: $T_A = 25^\circ\text{C}$; $V_{IN} = 15$ to 450V , $C_{OUT} = 0.01\mu\text{F}$

Symbol	Parameter	Min	Typ	Max	Unit	Conditions
V_{OUT}	Output Voltage Trim Range ¹	8		12	V	No load
ΔV_{OUT}	Load Regulation at 8V trim ¹		200	400	mV	$V_{IN}=15\text{V}$, $I_{OUT}=0$ to 1.0mA
ΔV_{OUT}	Load Regulation at 12V trim ¹		100	400	mV	$V_{IN}=50\text{V}$, $I_{OUT}=0$ to 3.0mA

Notes:

1. Guaranteed by design, not tested in production.
2. Pulse test duration < 1msec, Duty cycle < 2%

General Description

(continued from page 1)

The adjustable voltage version allows trimming of the output voltage from 8V to 12V. This version can also be connected to an external depletion mode MOSFET for increased output current. When used in conjunction with Supertex depletion mode MOSFET #DN2540N5, up to a 150mA is achieved.

LR6: SMPS Start-Up Circuit

One of the main applications for the LR6 is a start-up circuit for off-line switch-mode power supplies (SMPS) as shown in Figure 1. A minimum output capacitance of $0.01\mu\text{F}$ is recommended for stability. The wide operating input voltage range of the LR6 allows the SMPS to operate and start-up from rectified AC or a DC voltage 15V to 450V without adjustment.

During start-up, the LR6 powers the V_{CC} line of the PWM IC with a nominal output voltage of 10V. The auxiliary voltage connected through a diode to the V_{OUT} pin of the LR6 will start to increase. When the auxiliary voltage becomes larger than the output voltage the LR6 turns OFF its internal high voltage input line and output voltage allowing the auxiliary voltage to power the V_{CC} line of the PWM IC. The input current drawn by the LR6 from the high voltage line after start-up will therefore only be leakage current of the internal MOSFET switch, which is typically $0.1\mu\text{A}$.

The 3-terminal version shown in Figure 1 has load regulation guaranteed from 0 to 3.0mA at a fixed nominal output voltage of 10V. Applications requiring higher output current and/or a different output voltage can use the 8 pin adjustable version.

LR6: High Current SMPS Start-Up Circuit

The 8 pin version of the LR6 has connections for an external depletion-mode MOSFET for higher output current and external resistors for adjustable output voltage. As shown in Figure 2, the output current is increased to 150mA by using the Supertex 400V depletion-mode MOSFET DN2540. The maximum operating input voltage will be limited by the drain-to-source breakdown voltage of the external MOSFET, but cannot exceed the 450V rating of LR6.

The output voltage can be adjusted from 8V to 12V with 2 external resistors, R1 and R2. The ratio of $R2/R1$ determines the output voltage. R2 is connected between the V_{OUT} and Trim pins. R1 is connected between Trim and GND pins. Figure 5 is a curve showing output voltage versus resistor ratio $R2/R1$. The optimum range for $R1 + R2$ is $200\text{K}\Omega$ to $300\text{K}\Omega$. This minimizes loading and optimizes accuracy of the output voltage. Figure 5 uses an $R1 + R2$ of $250\text{K}\Omega$.

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Figure 1: SMPS Start-Up Circuit

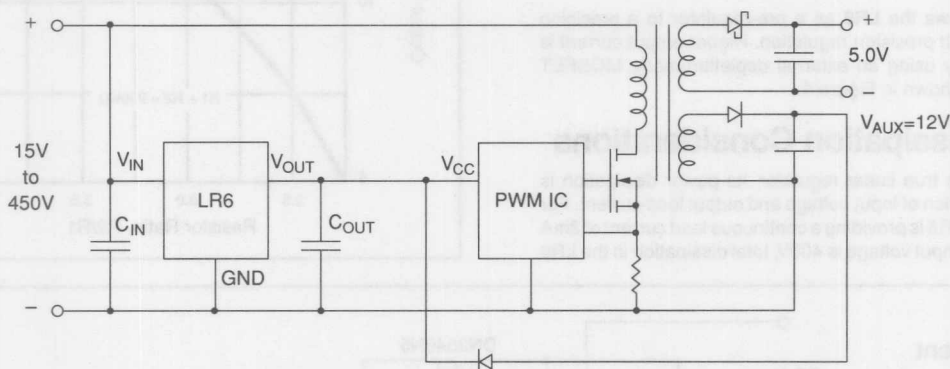
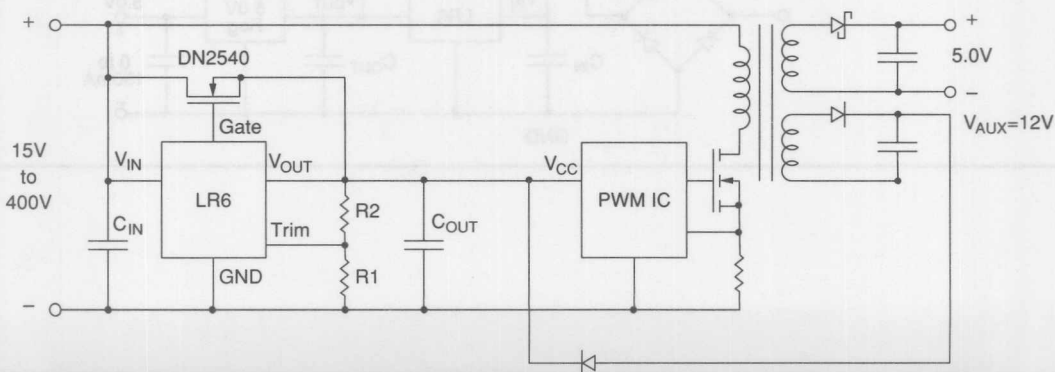


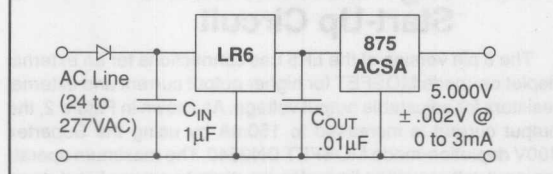
Figure 2: High Current SMPS Start-Up



LR6: Off-Line Linear Regulator

(Continued from page 3)

Figure 3: Cascading for Precision



LR6: Off Line Linear Regulator

Circuits requiring low voltages to operate logic and analog circuits benefit from the LR6. The conventional use of step down transformers can be eliminated thereby saving space and cost. Some examples of these applications are proximity controlled light switches, street lamp control, and low voltage power supplies for appliances such as washing machines, dishwashers, and refrigerators.

The wide operating input voltage range of 15V to 450V as well as the ripple rejection ratio of 50dB minimum allow the use of small high voltage input capacitor. The input AC line can be either full-wave or half-wave rectified. A minimum output capacitance of 0.01µF is recommended for output stability.

Figure 3 shows the LR6 as a pre-regulator to a precision regulator for high precision regulation. Higher output current is also possible by using an external depletion-mode MOSFET DN2540N5 as shown in Figure 4.

Power Dissipation Considerations

The LR6 is a true linear regulator. Its power dissipation is therefore a function of input voltage and output load current. For example, if the LR6 is providing a continuous load current of 3mA at 10V while its input voltage is 400V, total dissipation in the LR6

will be:

$$\begin{aligned} P_{DISS} &= (V_{IN} - V_{OUT}) \times (I_{OUT} + I_{MAX\ QUIESCENT}) \\ &= (400V - 10V) \times (3.0mA + 150\mu A) \\ &= 1.23 \text{ Watts} \end{aligned}$$

The 1.23 watts is for continuous operation. This is within the dissipation capabilities of the TO-220 and SOT-89 packages. See Page 2, thermal characteristics, for deratings.

For SMPS start-up applications, the output current is usually required only during start-up. This duration depends upon the auxiliary supply output capacitor and C_{OUT} but is typically a few hundred milliseconds.

All package types of the LR6 have been characterized for use with a C_{OUT} of at least 10µF, and an AC line of 277V.

Figure 5:
Typical Output Voltage vs. Resistor Ratio

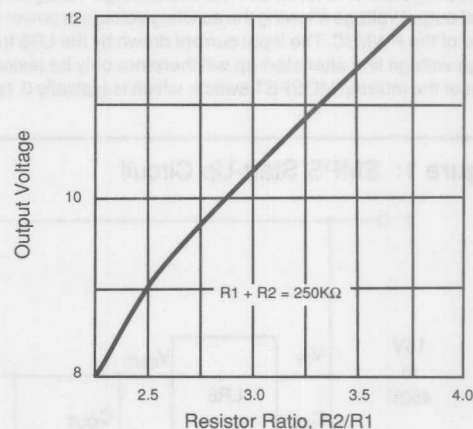


Figure 4:
High Current
Regulator

